

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajat Chauhan, et al. Art Unit: 2819
Serial No.: 10/710,874 Examiner: Anh Q. Tran
Filed: 08/10/04 Docket: TI-38154
For: REDUCING COUPLING EFFECT ON REFERENCE VOLTAGES WHEN OUTPUT
BUFFERS IMPLEMENTED WITH LOW VOLTAGE TRANSISTORS GENERATE HIGH
VOLTAGE OUTPUT SIGNALS

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

AMENDMENT

In response to the Official Action in this case mailed
February 27, 2006, please enter the following: